IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

licant : Shunpei Yamazaki, et al. Art Unit Examiner: W. David Coleman  $\mathcal{H}$ 

Serial No.: 08/994,038

: December 18, 1997

CHARGE TRANSFER SEMICONDUCTOR DEVICE AND MANUFACTURING

METHOD THEREOF

## Box AF

Commissioner for Patents Washington, D.C. 20231

## **AMENDMENT**

In response to the Official Action dated July 3, 2001,

Paper No. 22, in the above-referenced application, please amend the above-identified application as follows.

## In the Claims:

Please amend the claims as fo

1. (Amended) A charge, transfer semiconductor device

comprising:

a crystalline semiconductor film having a plurality of crystals extending in a crystal growth direction;

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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